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CONFIRMATION NO. 8030

SERIAL NUMBER 10/574,030	FILING or 371(c) DATE 03/27/2006 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. NL03 1167 US1		
APPLICANTS Robertus Theodorus Franciscus Van Schaijk, Leuven, BELGIUM; Michiel Slotboom, Leuven, NETHERLANDS;						
** CONTINUING DATA ***** This application is a 371 of PCT/IB04/51795 09/20/2004						
** FOREIGN APPLICATIONS ***** EUROPEAN PATENT OFFICE (EPO) 03103607.2 09/30/2003						
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 11/08/2006						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/HSIN-YI HSIEH/</u> Examiner's Signature		<input type="checkbox"/> Met after Allowance Initials	STATE OR COUNTRY BELGIUM	SHEETS DRAWINGS 3	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 3
ADDRESS NXP, B.V. NXP INTELLECTUAL PROPERTY DEPARTMENT M/S41-SJ 1109 MCKAY DRIVE SAN JOSE, CA 95131 UNITED STATES						
TITLE Two-transistor memory cell and method for manufacturing						
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